
Release Notes

Incremental update of Processor Expert Software for i.MX Processors v1.x

i.MX 6UltraLite v1.1 Service Pack

Product Content

- CPUs – iMX 6UltraLite in 14x14 package
 - MCIMX6G3DVM05AA, MCIMX6G3CVM05AA
 - MCIMX6G2DVM05AA, MCIMX6G2CVM05AA, MCIMX6G2AVM05AA, MCIMX6G2AVM07AA
 - MCIMX6G1CVM05AA, MCIMX6G1AVM05AA, MCIMX6G1CVM07AA
 - MCIMX6G0DVM05AA
- CPUs – iMX 6 UltraLite in 9x9 package
 - MCIMX6G3DVK05AA, MCIMX6G3CVK05AA
 - MCIMX6G2DVK05AA, MCIMX6G2CVK05AA
- Boards supported
 - BGA EVK rev.C2 based on the i.MX 6UltraLite 14x14 (MCIMX6G3CVM05AA)
- PinMux configuration data for a PinSettings component based on the following sources:
 - iMX6UL_RM_Rev_0_DraftB.pdf Rev 1, 01/2016
 - IMX6ULAEC_Rev_0.1.pdf Rev 0.1, 01/2016
 - IMX6ULCEC_Rev_0.3.pdf Rev 0.3, 01/2016
 - IMX6ULIEC_Rev_0.1.pdf Rev 0.1, 01/2016
- **Known problems and limitations**
 - None.
- **Revision history**
 - Fixed 9x9 package supply pins locations of [K6] NVCC_DRAM_2P5 and [N6] NVCC_DRAM.
 - Support of device changes based on the last datasheets and RM Rev 0 Draft B, 01/2016; Only the content of peripheral registers has been updated in 6UL processor database files and SDK header file, no IOMUXC functional changes since the Service Pack Update (REV 2) 12/9/2015

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